

# **Substrate Developments for the Chemical Vapor Deposition Synthesis of Graphene**

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Since the isolation of graphene and numerous demonstrations of its unique properties, the expectations for this material to be implemented in many future commercial applications have been enormous. However, to date, challenges still remain. One of the key challenges is the fabrication of graphene in a manner that satisfies processing requirements. While transfer of graphene can be used, this tends to damage or contaminate it, which degrades its performance. Hence, there is an important drive to grow graphene directly over a number of technologically important materials, viz., different substrate materials, so as to avoid the need for transfer. One of the more successful approaches to synthesis graphene is chemical vapor deposition (CVD), which is well established. Historically, transition metal substrates are used due to their catalytic properties. However, in recent years this has developed to include many nonmetal substrate systems. Moreover, both solid and molten substrate forms have also been demonstrated. In addition, the current trend to progress flexible devices has spurred interest in graphene growth directly over flexible materials surfaces. All these aspects are presented in this review which presents the developments in available substrates for graphene fabrication by CVD, with a focus primarily on large area graphene.

### 1. Introduction

There are numerous approaches in which single- and bilayer graphene can be fabricated or isolated. Currently, the most popular synthesis process is chemical vapor deposition (CVD). The CVD method can yield relatively high quality graphene, has the potential to synthesize graphene on a large scale, is well established and is facile.[1] While the CVD process is for the most part straightforward, some specialist equipment is required and the process parameters and reactor configuration are also important. CVD, in essence, is a process in which gaseous reactants are used to deposit material onto a substrate surface. The use of CVD for the fabrication of graphene rapidly developed once graphene had been demonstrated experimentally. Metal catalysts, in particular transition metals, which had a highly successful history for the growth of carbon nanotubes, where the obvious choice in

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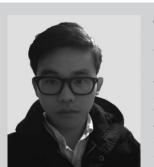
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early studies for the CVD catalytic growth of graphene. However, in many cases the direct growth of graphene over metal substrates is undesirable and transfer procedures introduce damage and so is for the most part also unwanted. This has led to the development of CVD techniques for the direct fabrication of graphene over different types of substrates. Indeed, the availability of substrates for graphene growth by CVD has matured significantly in recent years and numerous metals and nonmetals can be used. Moreover, these substrates can be used in their solid state and in many cases in a molten state. In addition, progress now includes direct graphene fabrication over flexible substrates. There are numerous reviews on the CVD synthesis of graphene and they, for the most part, tend to connect a broad discussion on the synthesis of graphene, its properties and its application<sup>[2-6]</sup> or they remain broad in discussing the synthesis of graphene and focus on a more specific application, for an excellent example, secondary metal ion batteries[7] or graphene as a smart material. [8] Other reviews may focus on a specific form of graphene such as N doped graphene<sup>[9]</sup> or porous graphene.<sup>[10]</sup> However, a review focusing on the substrates for the direct growth of graphene by CVD is lacking. This review addresses the developments in substrate systems for the growth of graphene by CVD, with a focus on large area graphene.

# 2. The CVD Process in Brief

CVD growth of graphene is a chemical process in which intricately mixed homogeneous gas phase and heterogeneous surface reactions are involved.<sup>[11]</sup> In the case of graphene, the gaseous precursor undergoes pyrolysis to form carbon species. These carbon species are then carried and deposited on to the surface of the substrate where nucleation occurs and then forms (grows) into the carbon structure of graphene. To improve the pyrolysis or decomposition of the precursor usually a catalyst is used to reduce the reaction temperature, but this is not a prerequisite. In general, the process for CVD graphene fabrication consists of eight steps: 1) mass transport of the reactant, 2) reaction of the precursor, 3) diffusion of gas molecules, 4) adsorption of the precursor, 5) diffusion of the precursor into the substrate (in many cases this is not true where the solubility is limited), 6) surface reaction, 7) desorption of products and 8) removal of the by-products. [12,13] Often thermal CVD is used, where, as the name suggests, heating is the driving system for the reaction. However, the use of a plasma (viz., plasma enhanced CVD, PECVD) can help reduce pyrolysis temperatures. In terms of operation pressure, two broad categories exist, namely, atmospheric pressure CVD (APCVD) and low pressure CVD (LPCVD). The reaction pressure, along with flow rates, temperature and growth time can affect the final quality of the as produced graphene. The choice of substrate is also important and different substrates can be used. The most successful are metals, in particular Cu as it serves as a catalyst and also, the low carbon adsorption propensity of Cu allows for easier control to form large area homogeneous mono or bilayer graphene. The main drawback for graphene fabricated over Cu is that, usually, the graphene needs to be transferred off the substrate (e.g., for device fabrication)



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and this processing step can incur contamination and/or damage, which leads to reduced performance of the graphene. Thus, the potential to directly grow graphene on nonmetallic



substrates by CVD is critical to expand the application potential of graphene.

# 3. Substrate Systems for Graphene Synthesis by CVD

The activity of transition metals forming graphitic material from heterogeneous catalytic reactions has been known for years from industrial processes. In addition, their successful implementation for carbon nanotubes is also well known and so transition metals where initially used as substrates for graphene growth. The choice of transition metal depends on the end goal (e.g., mono layer, bilayer, few layer graphene films) and consideration of the metals stability at high temperature, and carbon solubility are important along with their catalytic ability, which lowers the activation energy pathways for the reactions in the CVD process. In order to help control issues related to high carbon solubility, various metal alloys have been developed, and these are also discussed in the following subsections.

Metal substrates, although highly successful for graphene growth, when using the graphene for device based applications suffer a significant drawback, namely, the need for transfer which introduces defects and/or contamination which is counterproductive in that this diminishes the performance of the graphene. Hence, considerable efforts have been directed to the growth of continuous and homogeneous growth of graphene directly on dielectric substrates such as hBN, Si/SiO<sub>2</sub>, Al<sub>2</sub>O<sub>3</sub>, GaN, MgO, Si<sub>3</sub>N<sub>4</sub>, etc. Growth of graphene directly on these substrates is more challenging, none-the-less, great strides have been made, and this is also discussed in the sections below. Moreover, the growth of graphene over molten substrates and flexible substrates is also presented. In addition, it should be noted, that unless explicitly stated, most discussion centers on large area (poly crystalline) graphene. We begin with discussion on metal surfaces.

## 3.1. CVD over Metal Substrates

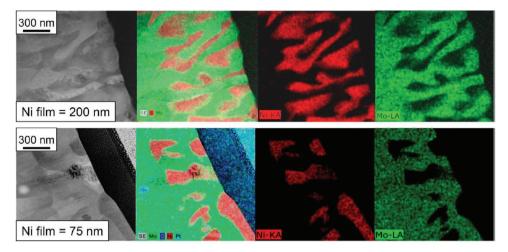
While there are a huge number of metal catalysts to choose from in the periodic table of elements (91 of the 118 elements listed in the periodic table), the most successful for the growth of graphene tend to be from the transition metals. Early CVD growth focused on solid metals, in particular Ni and Cu. However, it soon became clear that liquid metals can also be used and that they may have certain advantages. We now briefly look at solid metals first and then molten or liquid metals.

#### 3.1.1. Solid Metal Substrates

Ni was a highly successful metal in the CVD synthesis of carbon nanotubes and, thus, it was no surprise that early research for the CVD synthesis of graphene exploited Ni. Typically, polycrystalline Ni films are used and prior to synthesis they are annealed in an Ar/ $\rm H_2$  atmosphere ( $\approx 900-1000$  °C) to reduce the surface oxide and also increase the grain size. For growth,

the Ni film then requires a carbon feedstock, usually this is CH<sub>4</sub>. The use of Ar is not necessary, but can be implemented. Ni has a relatively high C solubility, so that as the CH<sub>4</sub> decomposes it produces C species (catalytically aided by the Ni), which then dissolve in the Ni film at the elevated temperatures.<sup>[14]</sup> This is followed by a cooling down step during which C atoms diffuse out from the Ni-C solid solution and precipitate on the Ni surface and form graphene films. In other words, graphene films form on Ni through a carbon segregation-precipitation process. Multi- or single-layer graphene can be grown on Ni, and can be controlled by the cooling rate.[15-17] However, the graphene layer numbers tend not to be homogeneous due to excess C dissolving out at grain boundaries leading to multilayer nucleation. For this reason, a preannealing treatment is applied to reduce the number of grain boundaries. In addition, the growth time and hydrocarbon concentration can also affect the graphene layer number formation.<sup>[5]</sup> One can also use very thin Ni films which then by default can only absorb a certain amount of carbon and this limits C precipitation. None-theless large area homogeneous single-layer graphene remains difficult to obtain.<sup>[18]</sup> A neat tactic to overcome this issue is to use Mo along with Ni, e.g., by depositing Ni on a Mo foil to produce a binary catalytic substrate. The technique yields large area homogeneous monolayer graphene. [19] Systematic studies show that during the APCVD process, the Ni and Mo films break up, diffuse and mix as independent islands. Graphene nucleation occurs exclusively over the Ni islands while the Mo islands around the Ni soak up excess C (see Figure 1). [20] Thus, C precipitation is controlled in a useful way enabling large area single-layer graphene to form.

Single-crystal Ni (111) can be used for homogeneous single-layer graphene formation,[21] however, this is difficult to translate to large area single crystalline growth. Aside from Ni. a number of other metal substrates with different carbon solubility and catalytic effect can be used. These include Ru.<sup>[22]</sup> Ir,[23] Pt,[24] Co,[25,26] Pd,[27] and Re.[28] Varying degrees of success have been obtained with these. A breakthrough came with the use of polycrystalline Cu foils as a substrate since it could relatively easily deliver high-quality homogeneous single-layer graphene at low cost and relatively easy transfer.[5,29] In the case of Cu as a substrate, while obtaining homogenous singlelayer graphene is easier, growth parameters such as gas flow are crucial. Unrestricted gas flows in APCVD lead to inhomogeneous layer numbers. An alternative configuration in which one end of the inner tube (in which the sample is placed) is closed so as to restrict the gas flow leads to a homogeneous graphene layer number over large areas.<sup>[30]</sup> The study showed the graphene layer number to depend on the sample placement, yielding either homogeneous monolayer or bilayer graphene. The data showed that local conditions play a role on layer homogeneity such that under quasistatic equilibrium gas conditions not only is the layer number stabilized, but the quality of the graphene improves. In another systematic study, again using Cu as the substrate in APCVD the flow rates and relative gas ratio of CH<sub>4</sub> to H<sub>2</sub> were explored and two very different growth windows are identified. For relatively high CH<sub>4</sub> to H<sub>2</sub> ratios, graphene growth is relatively rapid with an initial first full layer forming in seconds. Thereafter new graphene flakes nucleate and then grow on top of the first layer.



**Figure 1.** STEM investigations of sample cross-sections (lamellas). Furthermost left: STEM images of sections of lamellas. Next to these are elemental EDS maps (in false color). Reproduced with permission: [20] Copyright 2013, American Chemical Society.

The stacking of these flakes versus the initial graphene layer is mostly turbostratic. This growth mode can be likened to Stranski–Krastanov growth. While with relatively low  $CH_4$  to  $H_2$  ratios, the growth rates are reduced due to a lower carbon supply rate. In addition, bi-, tri-, and few-layer flakes form directly over the Cu substrate as individual islands. Etching studies were conducted and they showed that in this growth mode subsequent layers form beneath the first layer presumably through carbon radical intercalation. This growth mode is similar to that found with Volmer–Weber growth and produces highly oriented AB-stacked graphene. [31] **Figure 2** shows examples of the cross parametric studies and the two growth modes.

# 3.1.2. Liquid Metal Substrates

One of the disadvantages of metal substrates for large area CVD growth of graphene is the presence of microstructure defects such as grain boundaries. Due to differing C solubility at substrate defects (e.g., grain boundaries) during the growth process, uniform large area graphene films are challenging due to excess C precipitation during the cooling down process. In addition, substrate crystal orientation can affect graphene growth too (predominantly through epitaxial considerations). In the case of a liquid metal substrate, these aspects can be significantly reduced. Liquids tend to adopt a random close packed geometry and enable a quasi-atomically smooth surface (with no grain boundaries) through the combined action of surface tension and the thermal motion of atoms.[32,33] In terms of graphene growth, a liquid surface offers a simpler approach to form high quality uniform graphene. [34-36] On liquid surfaces the surfaces, the thermal motion of the atoms means the interatomic distances fluctuate<sup>[37]</sup> and thus weaken the interaction between the liquid (substrate) surface and the adsorbed atoms (C species in the case of graphene growth), thus decreasing the migration barrier energy. [38] This means the diffusion rate for C atoms is greatly accelerated allowing for the controlled growth of single crystal graphene islands with novel and changeable morphologies.<sup>[39]</sup> Moreover, H species also migrate very

efficiently and so results in a unique etching of developing graphene islands during growth leading to interesting fractal etching  $^{[40]}$  and regular etching.  $^{[41]}$ 

An early demonstration was the use of liquid p-block elements (e.g., Ga) for the synthesis of monolayer graphene using APCVD. [42] The electron mobility of single crystal domains grown on liquid Ga surfaces was as high as 7400 cm² V<sup>-1</sup> s<sup>-1</sup> under ambient conditions indicating high quality single crystal graphene flakes can be obtained from this approach. The technique is relatively simple and does not require film deposition or vacuum systems. A later systematic study with liquid metals showed they are highly suited for strictly single layer graphene. Examples are shown in **Figure 3**. [43] This is because during cooling from the CVD process, the surface metal solidifies quickly blocking the precipitation of absorbed carbon. As a result, growth is a self-limited catalytic process and moreover, is robust to variations in growth parameters.

The rheological surface of a liquid metal surface allows for the rotation, alignment and movement of growing graphene grains (islands). For example, Fu and co-workers<sup>[44]</sup> showed that adjacent graphene islands could merge without grain boundaries via a self-adjusting rotation process and thus assemble the crystals in a superordered and self-aligned manner.<sup>[45]</sup> The weak atomic interaction along with the high-vapor pressure of liquid metals allows for intermediary-free graphene fabrication<sup>[46]</sup> and results in very clean graphene which is attractive for its practical application.<sup>[36]</sup>

# 3.2. CVD over Nonmetal Substrates

As mentioned earlier, in order to avoid the need to transfer graphene which can damage the graphene as well as leave unwanted surface contamination, the CVD synthesis of graphene over nonmetal substrates is also important, particularly for electronic device fabrication where it is crucial to avoid metallic impurities and transfer contamination which will reduce costs and time.<sup>[47]</sup> It is worth noting that graphene/Si and graphene/Ge appear to be promising candidates for

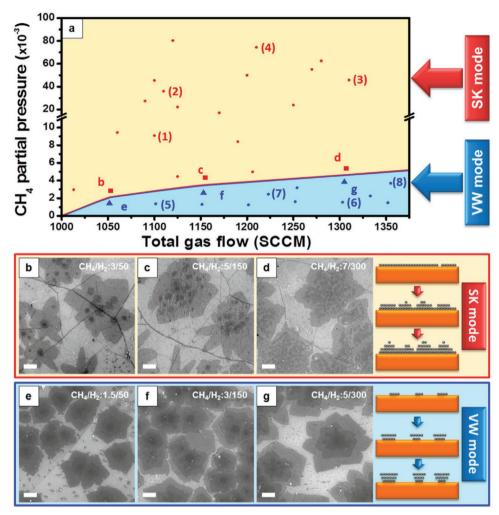


Figure 2. Relative gas flow windows used to investigate the dependence of graphene growth modes with respect to the  $CH_4$  partial pressure (estimated by the flow rate of  $CH_4$  divided by the total flow rate) versus the total gas flow a) (total gas flow includes  $CH_4$ ,  $H_2$ , and a constant flow 1000 SCCM of Ar). The small dots indicated all the measured points. b–d) A set of SEM images showing SK-like bilayer graphene growth corresponding to b–d) red square spots in (a). The graphene flakes irregular in shape in the SK-like mode. e–g) A set of SEM images showing VW-like bilayer graphene growth corresponding to e–g) blue triangle spots in (a). The graphene flakes are regular in shape in the VW-like mode. All scale bars are 2  $\mu$ m. Reproduced with permission. [31] Copyright 2016, American Chemical Society.

transistors because of the adjustable Schottky barrier, which forms between graphene and semiconductor.<sup>[47]</sup>

Early work with nonmetal substrates showed the potential of a number of oxides, namely, SiO<sub>2</sub>, Al<sub>2</sub>O<sub>3</sub>, MgO,Ga<sub>2</sub>O<sub>3</sub>, and ZrO as substrates for graphene growth.<sup>[48]</sup> The study suggested that oxides do have a catalytic role to play, and subsequent studies at the low temperature of 325 °C confirmed that oxide base substrates do provide some catalytic role.<sup>[49]</sup>

#### 3.2.1. Solid Nonmetal Substrates

Most CVD approaches with nonmetal substrates use solid substrates. One of the more important substrates is Si wafers  $(Si/SiO_x)$ . While various studies have been conducted to achieve this directly on the  $SiO_x$  surface, to achieve large area homogeneous monolayer graphene is challenging. One approach implemented the use of  $O_2$  to aid an APCVD process

which yielded polycrystalline monolayer graphene.<sup>[50]</sup> Another APCVD study (also with CH<sub>4</sub> as the precursor) obtained few-layer graphene.<sup>[51]</sup> With near equilibrium CVD, researchers found single crystal graphene flakes with hexagonal and dodecagonal shapes over the SiO<sub>x</sub> surface.<sup>[52]</sup> However, large area, homogeneous monolayer graphene is particularly difficult to obtain. To overcome this issue, a confinement technique in which two Si/SiO<sub>x</sub> wafers with their oxide faces in contact in a sandwich configuration was used to yield homogeneous single-layer large area graphene.<sup>[53]</sup> The graphene is polycrystalline and the grain boundaries are facetted (see **Figure 4**) indicating further improvements are needed for single-layer material, but this is still a positive development.

To reduce the CVD synthesis temperature, plasma-enhanced CVD (PECVD) can be used. A low temperature (550–650 °C) PECVD process was demonstrated by a number of investigators. [54–56] PECVD can also be used for the growth of graphene nanowalls. [57] One study, showing growth

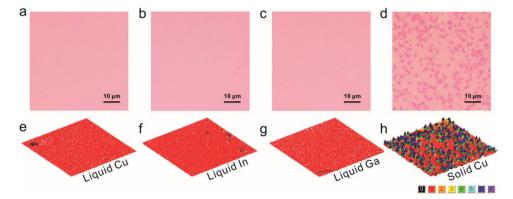


Figure 3. Typical growth results on liquid or solid metal substrates. a–c) Optical microscope images of graphene grown on liquid Cu, In, and Ga, respectively, which demonstrates the excellent uniformity of the single-layer graphene. d) Optical microscope image of graphene grown on solid Cu foil under ambient pressure, which indicates poor uniformity. All the graphene films were transferred onto 300 nm SiO2/Si substrates for characterization. The scale bars are 10 μm. e–h) Layer distribution determined by RGB color analysis of the corresponding optical microscope images (a–d), respectively. The layer thickness is represented by different colors as seen from the bottom color codes. Reproduced with permission.<sup>[43]</sup> Copyright 2014, American Chemical Society.

temperatures even as low as 400  $^{\circ}\mathrm{C}$  showed micrometer-sized graphene crystals forming the walls.

Graphene can also be directly grown on  $Al_2O_3$  wafers, although this typically requires very high temperatures for good quality (poly-crystalline) graphene. [58]  $Al_2O_3$  is attractive in that it can serve as a device substrate, viz., a device may be fabricated directly on the synthesized graphene without the need to invoke a transfer process. [47] The Al sites on  $\gamma Al_2O_3$  are highly reactive and act as catalytic sites. [59,60] Song et al. [61] presented an APCVD approach using CH<sub>4</sub> as the precursor in which they could obtain mono-layer graphene. Another group employed PECVD and obtained uniform graphene films. [54]

 $TiO_2$  is another substrate explored for its potential for graphene synthesis by CVD. The direct synthesis of graphene was first demonstrated over r-TiO2 after wet etching followed by flattening to atomic smoothness over the (001), (110), and (100) faces. [62] The

graphene quality was found to be superior for the (110) face under the same growth parameters. Another group showed that graphene fabrication of TiO<sub>2</sub> was possible for both APCVD and LPCVD for mono- and few-layer formation.<sup>[63]</sup> In the case of SrTiO<sub>3</sub> (STO) graphene film formation by CVD was shown through APCVD.<sup>[64]</sup>

The potential for graphene CVD growth at low temperatures on an oxide was demonstrated using MgO crystals at the low temperature of 325 °C using acetylene as the precursor. Such low temperature approaches are relevant to maintain the mechanical integrity of low-dielectric constant (K) intermetal dielectrics in transistor technology. Another, low temperature graphene fabrication, also on a high K dielectric material was demonstrated by the same team on ZrO<sub>2</sub>, again with acetylene and at a low temperature of 480 °C with acetylene as the precursor. Another group showed vertically grown graphene sheets could form on ZrO<sub>2</sub> through thermal CVD

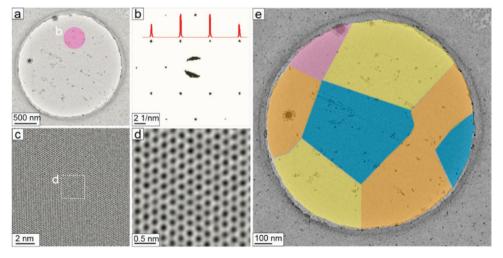


Figure 4. TEM characterizations of the large-area synthetic monolayer graphene (from sandwich configuration). a) Low-magnification micrograph of a graphene film transferred onto a holey carbon TEM grid. b) SAED pattern of the region circled in (a). The inset profile shows the intensity profile of the diffraction spots. c,d) High-resolution TEM images showing the honeycomb atomic configuration of graphene. e) False-color composite micrograph image highlighting the different domain (grain) orientations and faceted grain boundaries. Reproduced with permission.<sup>[53]</sup> Copyright 2017, American Chemical Society.

using methane or ethanol as the precursor.<sup>[66]</sup> This work was also interesting because typically PECVD is required for vertical graphene growth. Another technologically important substrate for graphene growth is silicon nitride (Si<sub>3</sub>N<sub>4</sub>). This was accomplished with a two-stage CVD process. The graphene sheets are seen to form independently and then later merge to form a polycrystalline film.<sup>[67]</sup> Graphene can also form on aluminum nitride. Direct growth of graphene was shown on another nitride, namely, AlN (AlN/Si (111)). In this case, propane served as the precursor and, as found with a number of non-metal catalysts, temperature is relevant in terms of the graphene quality, such that it improves as the temperature increases. In this case, 1350 °C was needed for high quality graphene.<sup>[68]</sup>

Graphene can also be grown on a number of traditional glasses and this could be important for a number of daily life applications. An APCVD approach demonstrated for the first time the direct well-controlled growth of high quality graphene on insulating solid glasses was possible. Moreover, the layer thickness could also be tuned.<sup>[69]</sup> PECVD can also be employed to grow graphene films directly over quartz (see **Figure 5**). Nanographene films with good uniformity were grown over 4 in. wafers.<sup>[54]</sup> Vertical graphene growth over quartz by CVD has also been shown over quartz substrates<sup>[66]</sup> and suggests it is a rather versatile substrate for graphene formation. In the case of vertical graphene, this can also occur over carbon buffer layers.<sup>[70]</sup>

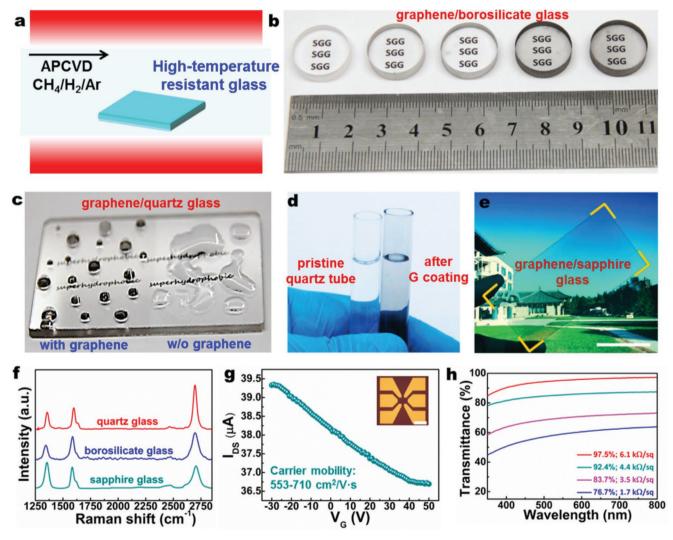


Figure 5. Catalyst-free APCVD growth of uniform graphene on various solid glasses. a) Schematic diagram of the catalyst-free APCVD growth method. b) Photograph of the borosilicate glass substrates before (leftmost) and after graphene growth with different CH4 flow rates at 2, 5, 7.5, and 10 sccm. c) Demonstration of the hydrophobic and hydrophilic nature of graphene/quartz glass (the left part) and the bare quartz glass, respectively. d) Photograph showing the differences in water containing behaviors between the graphene-coated and pristine quartz test tubes. e) Photograph of graphene/sapphire glass plate displaying a good transparency. Scale bar: 4 cm. f) Representative Raman spectra of directly grown graphene on different types of solid glasses. g) Transfer curve of the graphene FET; the inset shows an optical microscope image of an individual device. The graphene was grown on quartz glass under the following APCVD condition: Ar/H2/CH4, 100/50/8 sccm at 1020 °C for 3 h. Scale bar: 100 μm. h) Sheet resistance and UV–vis transmittance spectra in the wavelength range of 350–800 nm of the graphene/quartz glass. Reproduced with permission. [69] Copyright 2015, American Chemical Society.



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#### 3.3. Molten Nonmetal Substrates

It is believed that by using a molten liquid insulating substrate, the uniform nucleation and accelerated growth of graphene can occur. This was first shown on soda-lime glass (which has a low softening point of 620 °C). Soda lime glass is cheap and inexpensive and is used in various applications.<sup>[71]</sup> In this case an APCVD approach was implemented. The application potential of the as-produced graphene on soda-lime glass was also demonstrated for smart heating-devices such as transparent defoggers and thermochromic displays, and for biocompatible cell culture mediums. The same team extended the growth of uniform graphene (over soda-lime glass) to show large area growth over 12 in. areas.<sup>[72]</sup>

#### 3.4. Flexible Nonmetallic Substrates

As for rigid nonmetal substrates, a key advantage of growing graphene directly on flexible substrates is the avoidance of transfer induced damage (e.g., cracks, tears, and wrinkles). Moreover, the direct growth of graphene over flexible substrates has significant promise for flexible and stretchable electronics. Examples include e-skin and health monitoring on humans. [73-75]However, there are constraints with some of these materials due to low temperatures being a prerequisite to avoid melting, deforming or damaging the substrate (e.g., with polyimide substrates). For some substrates, this is not so, e.g., mica and hBN, which we first look at. Nanographene films were grown on mica using a PECVD approach with CH<sub>4</sub> as the precursor at the relatively low temperature of 525 °C.[54] There is considerable interest in graphene growth over hBN because its lattice parameter is the same as for graphene. [8,76] The CVD growth of graphene over hBN as shown for hBN initially grown over a Cu substrate (and the Cu base substrate remained for the CVD growth of graphene, so strictly the substrate was hBN/Cu).[77] Ding et al.<sup>[78]</sup> developed a metal free CVD approach for few-layer graphene.<sup>[77]</sup> Similarly, Liu et al.<sup>[79]</sup> obtained few-layer graphene over hBN using cyclohexane as the precursor using an APCVD approach. Another approach is to grow graphene directly over hBN using seed-assisted growth. The use of the well-defined poly(methyl methacrylate) seeds enabled effective control over the nucleation densities and locations of the graphene domains on predeposited h-BN monolayers. This allowed the formation of patterned G/h-BN arrays or continuous films.[80]

For temperature sensitive flexible substrates such polyimide (PI) and polydimethylsiloxane (PDMS) metal capping layers are used to provide a catalyst-assisted CVD system and thus enable low temperatures to be used. Cu deposited on PI allowed a low temperature PECVD route at 300 °C. [81] While for PDMS Ni was deposited over the base PDMS. [7]

#### 4. Outlook

For graphene (and other van der Waals 2D materials) to fulfill their promise in applications, it will be crucial that they can be synthesized under appropriate conditions, such as, within certain temperature windows, and that this, in many cases,

can be achieved directly on the surface of the material in question, viz., on any substrate. This is a grand task. CVD is also a well-established technique and is versatile. As has been demonstrated in this review, researchers have been making significant strides to develop the method for graphene synthesis over a broad range of substrates under a variety of conditions and, moreover, have been pushing the boundaries to achieve this on at remarkably low temperatures. Still there are challenges. As was highlighted, the direct synthesis of graphene on polymers, which will be crucial for flexible based devices, is an example where improved CVD approaches are needed. This is likely an area where important developments will be seen in the near future. In addition, directly grown patterned graphene, for example for devices, will become ever more relevant and it is conceivable that CVD merged with another technique could be a key player. One could imagine CVD reactions operating slightly below the nucleation and growth thresholds in combination, with, for example, an electron beam or laser beam that can then overcome the thresholds at specific local regions and in this way yield patterned graphene growth. This same approach could be applied to other 2D materials and pave the way for patterned hetero 2D materials fabrications both in stacked and laterally stitched configurations. Another important aspect of graphene growth likely to be seen in the future is large area and wafer scale single crystal growth of graphene. This remains challenging, but the pace of change in this field suggests breakthroughs will emerge soon, and a key aspect for this is likely to be related to a mix of the right CVD reaction/reactor configurations and clever choice/implementation of substrate. Another aspect one can anticipate in the future is high speed growth of large area single crystal graphene by CVD. Moreover, future developments with substrate use in CVD will minimize or even remove the need for transfer and thus, enable the best graphene performance to be extracted in which ever application it is used.

#### 5. Conclusion

The changes seen in the growth of graphene by CVD have been rapid since the first evidence of single-layer graphene. CVD is a technologically established technique, and is relatively versatile and, is highly successful at producing graphene. Although initially metal substrates were required, in recent years this is no longer true and numerous dielectric substrates can be used for the successful growth of graphene by CVD. Moreover, significant inroads are being made in terms flexible substrates which will be a key technology field in the near future. In addition, it is likely that the patterned direct growth of graphene and large area single crystal graphene will be demonstrated soon. In all these developments (past, present, and future), substrate availability is crucial to the applications success not only of graphene, but also other 2D materials and their combinations to yield hetero 2D systems.

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# **Conflict of Interest**

The authors declare no conflict of interest.

# **Keywords**

chemical vapor deposition, graphene, substrate, support, synthesis

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